

**Publications (student co-authors underlined)**

1. S. Xue, Y. Li, L. Zhu, B. Sarangi, J. Sai Sandra, J. Rong, N. Mai, S. Chen, A. Chavda, U. Sambangi, J. Peram, P. Parthiban, and V. Selvamanickam, “Current Sharing in Double-sided REBCO Tapes”, *Supercond. Sci. Technol.* (2024). DOI 10.1088/1361-6668/ad4e76
2. P. Pathak, G. Majkic, and V. Selvamanickam, “Role of Customized Scan Strategies and Print Layers Dwell Time of Additively Manufactured 316L SS” *Proc. International Conference of Advanced Manufacturing (ICAM)*, Washington D.C. Oct. 30 – Nov. 3, 2023
3. M. Paidpilli, C. Goel, B. Sarangi, S. Chen, E. Galstyan, J. Jaroszynski, G. Bradford, D. Abraimov, and V. Selvamanickam, “40-meter-long REBCO Tapes with Critical Current Over 4,000 A/12 mm at 4.2 K and 13 T by Advanced MOCVD” *Superconductivity* **9**, 100081 (2024).
4. N. Castaneda, X. Wang, P. Ferracin, C. Funkhouser, E. Galstyan, H. C. Higley, S. R. Korupolu, G. Majkic, H. Nguyen, S. O. Prestemon, V. Selvamanickam, H. Truong, and X. Wang “A 6-around-1 cable using high-temperature superconducting STAR® wires for high-field magnet applications” *Supercond. Sci. Technol.* **37**, 035009 (2024).
5. P. Pathak, G. Majkic, T. Erickson, T. Chen, V. Selvamanickam, “2D-XRD and Micro-CT Characterization of 316L SS Fabricated using LENS-DED Additive Manufacturing”, *Additive Manufacturing* (in review)
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7. S. Peng, Y. Lin, Q. Feng, W. Fu, M. Li, S. Chen, M. Paidpilli, C. Goel, E. Galstyan, and V. Selvamanickam, “Autoregressive Distributed Lag-based Dynamic Uniformity Modeling and Monitoring in Superconductor Manufacturing”, *J. Manufacturing Systems* (in review).
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9. M. Li, S. Peng, Y. Lin, Q. Feng, W. Fu, S. Chen, M. Paidpilli, C. Goel, E. Galstyan, V. Selvamanickam, “Quantile-Regression Enriched Event Modeling Framework for Dropout Analysis in High-Temperature Superconductor Manufacturing”, *J. Intelligent Manufacturing* (2024) DOI: 10.1007/s10845-024-02358-7
10. V. Selvamanickam, “Engineering REBCO high temperature superconductor materials for applications” *Physica C*. **614**, 1354355 (2023).
11. J. sai Sandra, M. Paidpilli, S. Sharma, M. Suarez-Villagran, V. Yerraguravagari, J. Wosik, S. Sun, R. Jain, and V. Selvamanickam, “Applications-oriented Development of Buffer Architecture on Nonmetallic Substrates”, *IEEE Trans. Appl. Supercond.* **33**, 6602506 (2023)
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## **BOOK CHAPTER**

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